



The 10th Annual IEEE Information Technology, Electronics and Mobile Communication Conference

**IEEE IEMCON 2019
University of British Columbia, Vancouver, Canada
17 – 19 October 2019**

Special Session on Cryptography Hardware Accelerators

Organized by Hayssam El-Razouk (California State University, Fresno)

We are very much pleased to announce that a special session on *Cryptography Hardware Accelerators* will be held during the 10th Annual IEEE Information Technology, Electronics and Mobile Communication Conference (**IEEE IEMCON 2019**) from **17 to 19 October 2019** at **University of British Columbia, Vancouver, Canada**. The special session aims to bring together scholars with backgrounds from cryptography, computer arithmetic, hardware design, and machine learning, to emphasize dissemination of ongoing research in the fields of Hardware Design of Cryptography and Finite Field Arithmetic Accelerators. Contributed papers are solicited describing original works in above mentioned fields.

Accepted and presented papers will be submitted for publication to IEEE Xplore Digital Library (Scopus; EiCompendex; Web of Science and Google Scholar).

TOPICS OF INTEREST

Topics include but are not limited to:

- Finite field arithmetic hardware.
- Symmetric ciphers hardware.
- Asymmetric ciphers hardware.
- Cryptographic hash functions hardware.
- Low area Crypto and arithmetic processors implementations.
- Power efficient Crypto and arithmetic processors implementations.
- High speed Crypto and arithmetic processors implementations.
- Testing and validation of Crypto and arithmetic implementations.
- Fault tolerance and error detection/ correction Crypto and arithmetic implementations.

- Machine learning for crypto and arithmetic implementations.

Authors are invited to submit their latest findings in the areas of hardware implementations of cryptography systems and finite field accelerators.

CALL FOR CONTRIBUTED PAPERS

Prospective authors are invited to submit their full paper(s) electronically through EDAS. Papers will be reviewed by at least three referees for technical merit and content. Accepted papers must be presented at the conference by one of the author(s) whose name will appear in the proceedings. For further details, and information on paper categories, please visit our website.

SPECIAL SESSION ORGANIZER AND CHAIR

Hayssam El-Razouk (California State University, Fresno)

CONFERENCE URL

<http://iee-iemcon.org/>

REGISTRATION

Please visit our website: <http://iee-iemcon.org/registration/>

IEEE IEMCON PAPER CATEGORIES

1. Regular Paper – 7 pages maximum (3 additional pages allowed but at an extra charge)
2. Short Paper (Work-in-Progress) – 6 pages maximum (2 additional pages allowed but at an extra charge)
3. Poster – 5 pages maximum

PAPER SUBMISSION INFO

IEEE IEMCON uses EDAS for submission.

Authors need to:

1. Create an account (if not already registered) with EDAS at <http://edas.info>
2. Submission link : <https://edas.info/newPaper.php?c=25845&track=97413>

To check the submission page please go through the above mentioned **Conference flyer**.